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020457 7590 04/06/2004

ANTONELLI, TERRY, STOUT & KRAUS, LLP
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/760,704	01/17/2001	Hiroyuki Nakano	501.39474X00	5851

TITLE OF INVENTION: METHOD OF PROCESSING A SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/06/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
SEVER, ANDREW T	2851	356-336000

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	Antonelli, Terry, Stout & Kraus, LLP

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Renesas Technology Corp.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): individual corporation or other private group entity government

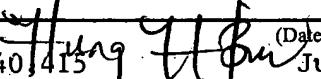
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